

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	26	control adj interface with (4-wire "4" adj wire adj joint adj test adj action adj group JTAG)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/07 20:31
L10	5	control adj interface with (4-wire "4" adj wire adj joint adj test adj action adj group JTAG) same (RFIC IC integrated LSI VLSI)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/07 20:13
L11	3	control adj interface with (4-wire "4" adj wire adj joint adj test adj action adj group JTAG) with chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/07 20:31